



533 Rec'd CT/PTO 12 JUL 2002

Docket No.: 60188-124

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yasuyuki DOI, et al.

Serial No.: 10/019,437

Filed: December 31, 2001

Group Art Unit: 2821

Examiner: not yet assigned

For: LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND METHOD FOR  
CONTROLLING THE SAME

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REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents  
Washington, DC 20231

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the Title has two errors. Attached is a copy of the Combined Declaration/Power of Attorney, filed on December 31, 2001, which evidences that the Title should read:

LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND  
METHOD FOR CONTROLLING THE SAME.

It is requested that a corrected filing receipt be issued.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

  
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## UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/019,437	12/31/2001	2821	1254	60188-124	11	14	4

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APR 19 2002

McDermott, Will & Emery

Jack Q Lever Jr  
McDermott Will & Emery  
600 13th Street NW  
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CONFIRMATION NO. 4255

REPLACEMENT FILING RECEIPT



\*OC000000007862482\*

Date Mailed: 04/12/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

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**Domestic Priority data as claimed by applicant**

THIS APPLICATION IS A 371 OF PCT/JP00/05904 08/31/2000

**Foreign Applications**

JAPAN 11/356898 12/16/1999

**Projected Publication Date:** Not Applicable, filed prior to November 29, 2000

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

DRIVING

BUFFERING

Liquid crystal drive circuit, semiconductor integrated circuit device, reference voltage buffer circuit, and method for controlling the same

**Preliminary Class**

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**LICENSE FOR FOREIGN FILING UNDER  
Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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Docket No. \_\_\_\_\_

**COMBINED DECLARATION/POWER OF ATTORNEY**  
**FOR PATENT APPLICATION**

As a below named inventor(s), I (we) hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled  
LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND METHOD FOR  
CONTROLLING THE SAME  
the specification of which

(check one)        ☐ is attached hereto.

☐ was filed on \_\_\_\_\_ as  
United States Application No. \_\_\_\_\_.

☒ PCT International Patent Application Number PCT/JP00/05904  
filed August 31, 2000  
and was amended on \_\_\_\_\_ (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
<u>11-356898</u> (Number)	<u>JAPAN</u> (Country)	<u>16/12/1999</u> (Day/Month/Year Filed)	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No
_____ (Number)	_____ (Country)	_____ (Day/Month/Year Filed)	<input type="checkbox"/> Yes <input type="checkbox"/> No